



## HIGH SPEED COMPUTING PROCESSOR MODULE

### Overview

Cyient's High Speed QorIQ Processors Based on Power Architecture Technology T Series Multicore NXP T2081 is a generic purpose processing and interface module with multiple communication interfaces, multipurpose IO interface and capable of hosting high end application software.

### Key Features

- T2081 NXP quad core processor Processing
- Hardware ready for ARINC 653 compliant/VxWorks OS
- Solid state switch data capture
- 17 channels of multipurpose IOs
- BITE test functionality

## Technical Specifications

### Memory:

- NOR Flash 1 Gb, 16 bit data, asynchronous memory on IFC interface
- FRAM (NVRAM): 2Mbit, 8 bit data SPI interface
- DDR3 SDRAM: Total 4Gbit, 128MB x 16 x 2 IC, 1.066 GHz data rate
- SRAM: 16Mb, 1M X 16 bit
- 512KB Core net platform cache
- 32KB data cache and 32KB instruction cache

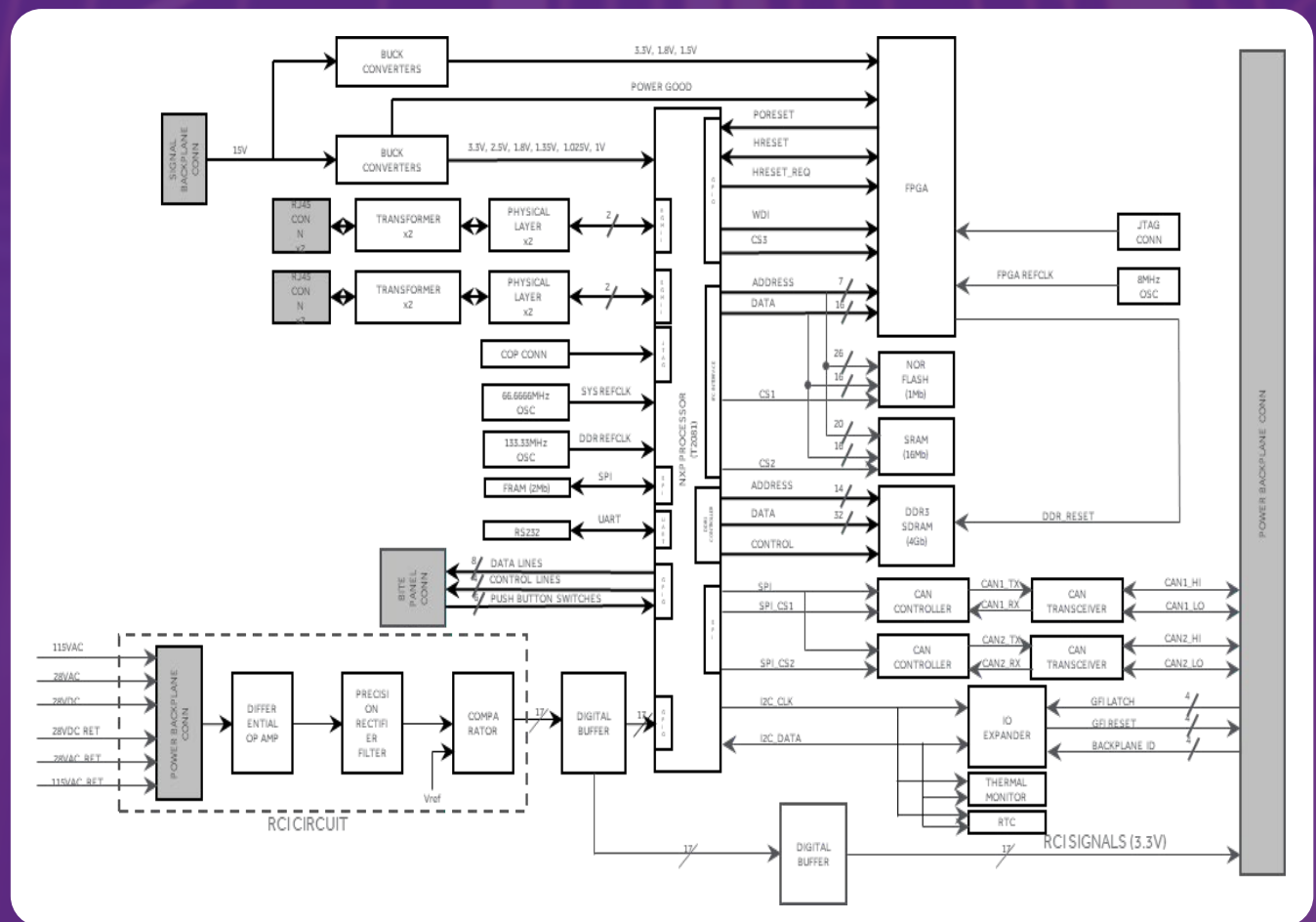
### Interfaces:

- I2C interface thermal monitor,
- RTC and Extender port IC (Programming of RTC through BIT panel)
- JTAG interface
- RGMII (EC1 and EC2) PHY and SGMII (SERDES) PHY Ethernet Interface
- SPI Interface CAN Controller
- UART Interface

### Size:

(LXHXW): 7.63 inches by 9.08 inches by 0.094

## Block Diagram



## Benefits

Overall "Low cost of ownership" to the customer due to

- Compact Processor module
- Multiple interfaces

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